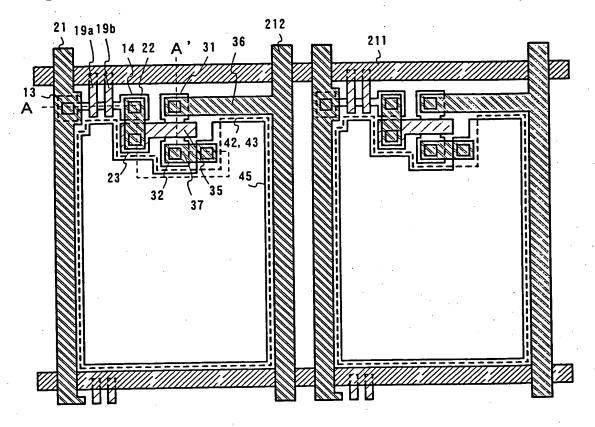
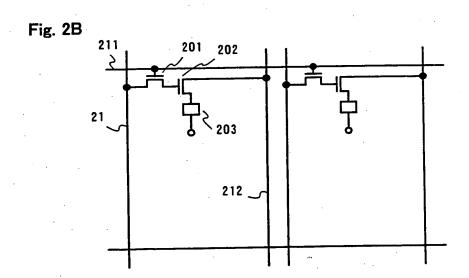
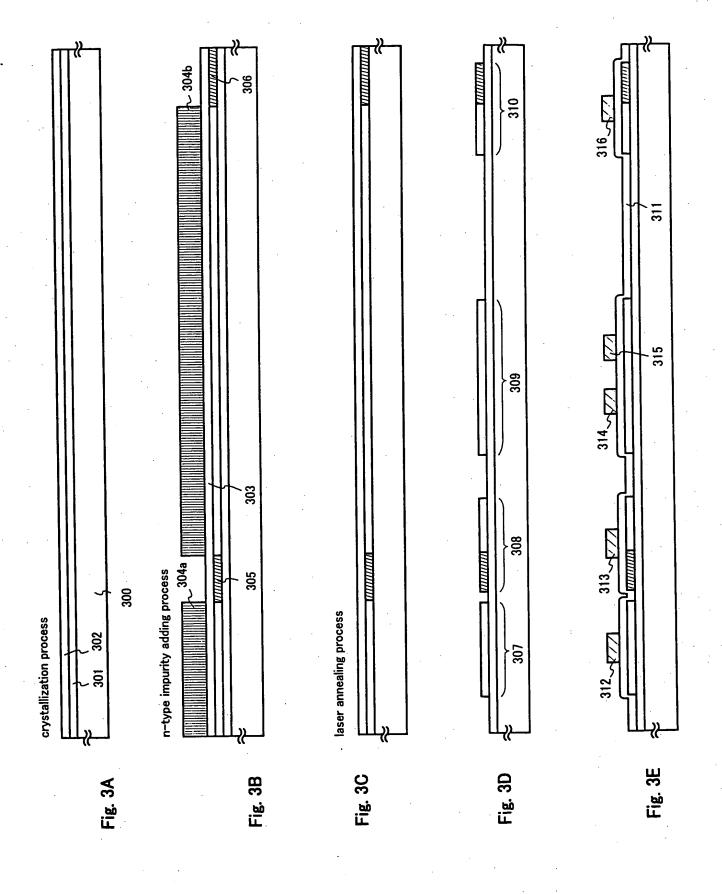
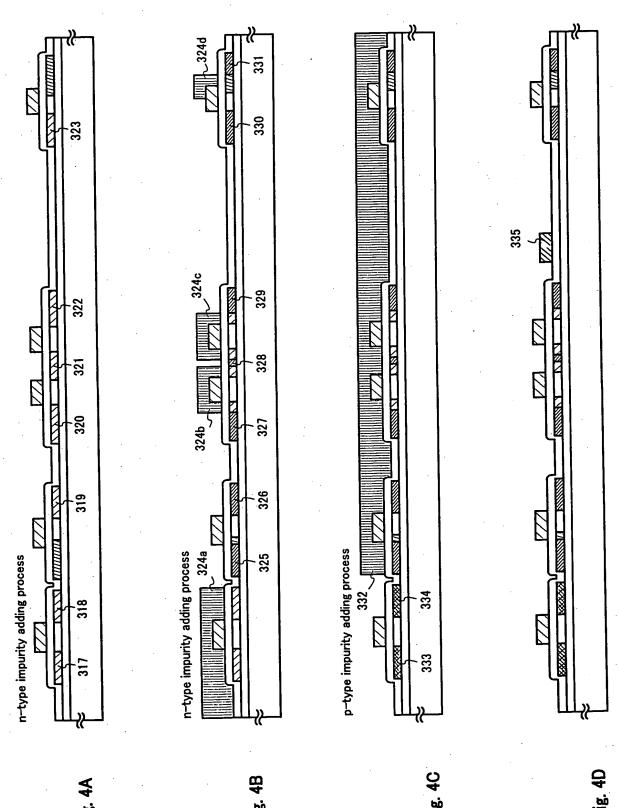
. 89

Fig. 2A









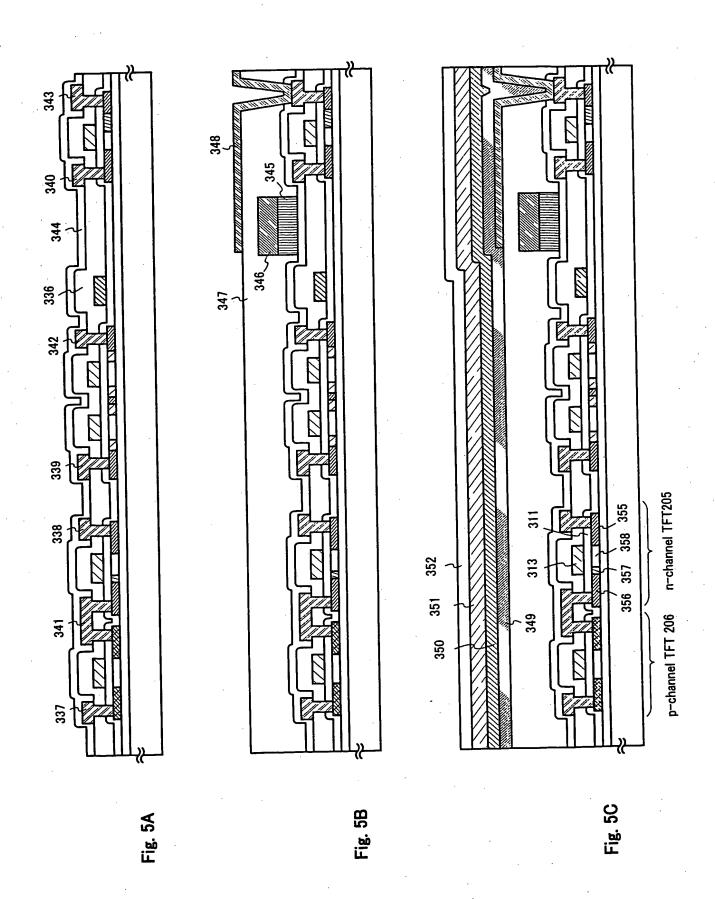
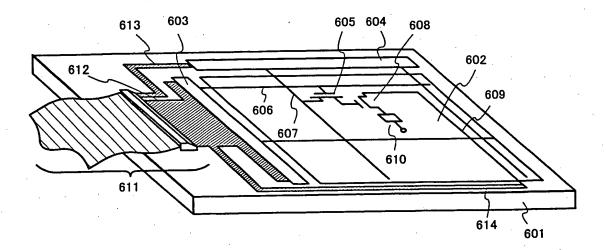


Fig. 6



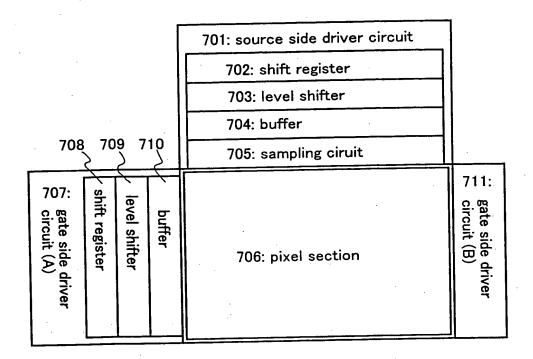


Fig. 8

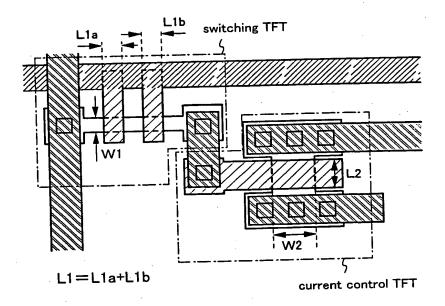


Fig. 9

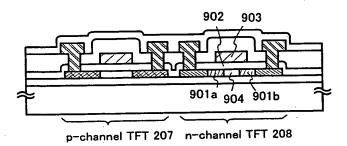


Fig. 10

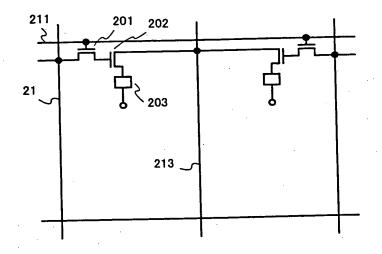


Fig. 1

Fig. 12A

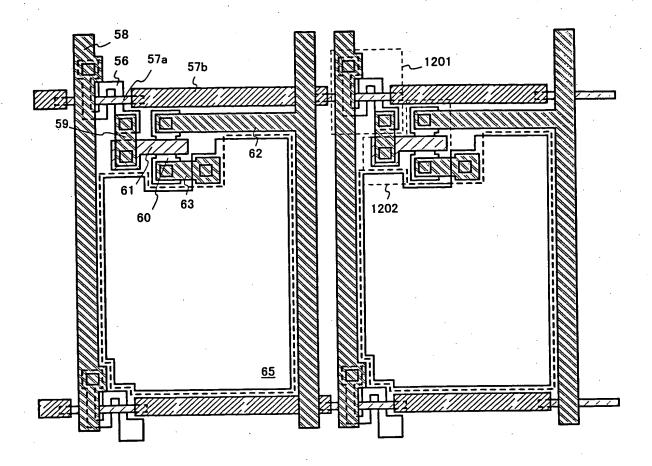
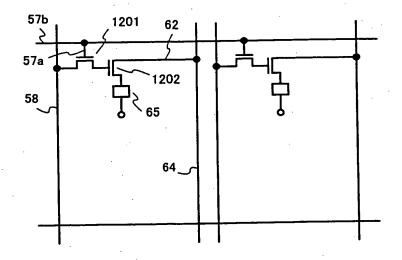
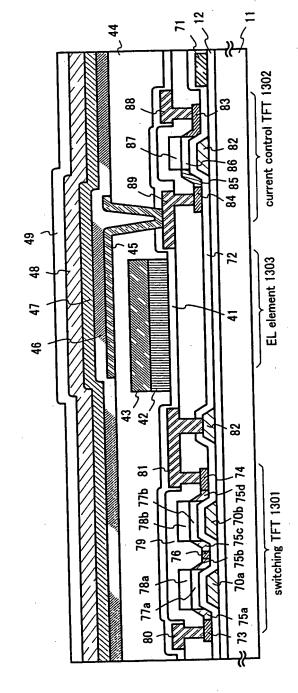


Fig. 12B





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Fig. 7

Fig. 15A

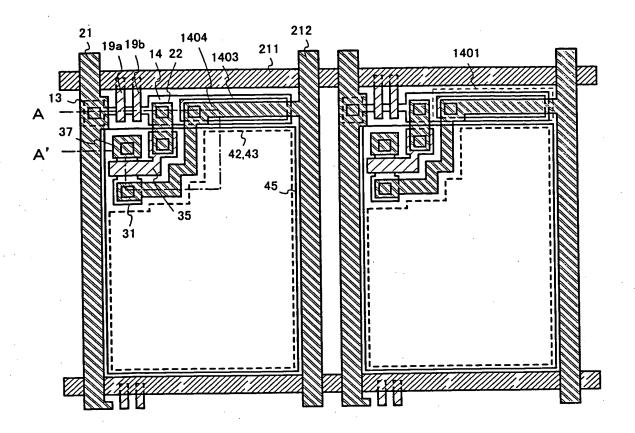


Fig. 15B

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Fig. 16A

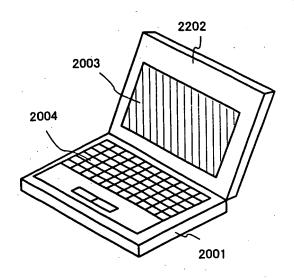


Fig. 16B

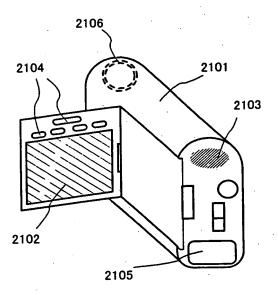


Fig. 16C

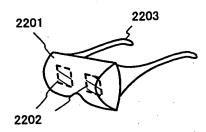
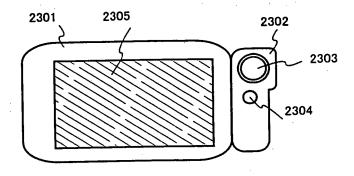


Fig. 16D



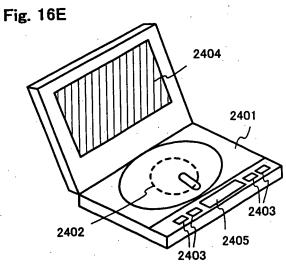


Fig. 16F

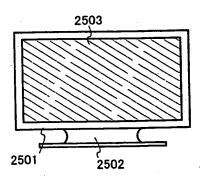


Fig. 17A

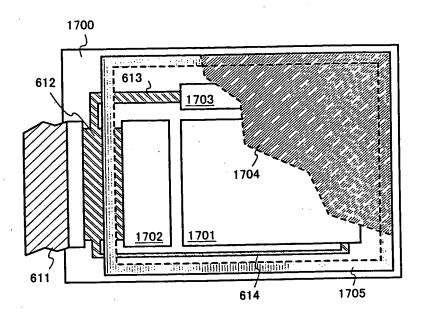


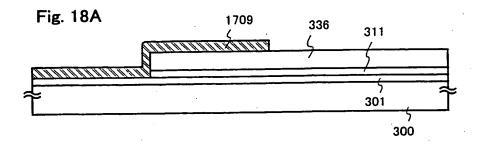
Fig. 17B

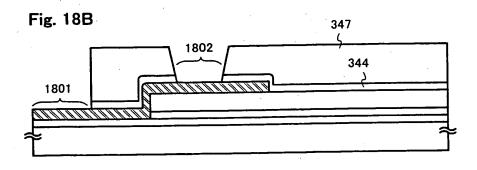
1706 1707 1704

611

1710 1709 driver circuit pixel portion 1705

1700





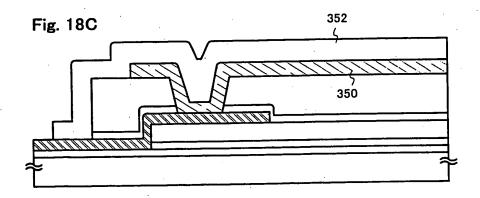
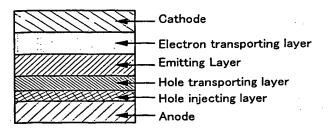


Fig. 19



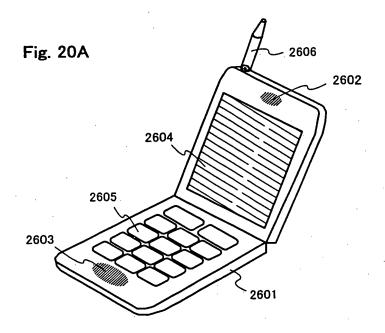


Fig. 20B

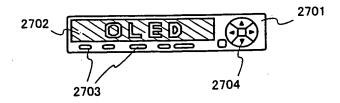


Fig. 21A

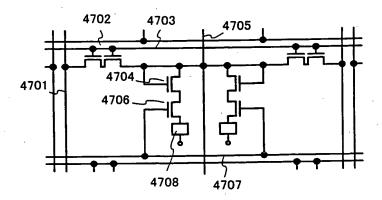


Fig. 21B

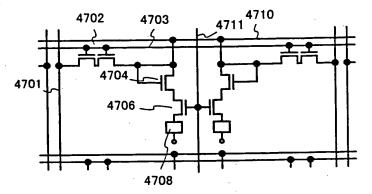


Fig. 22A

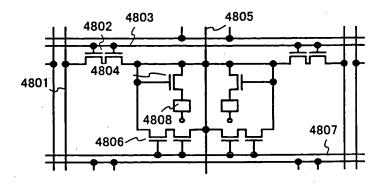
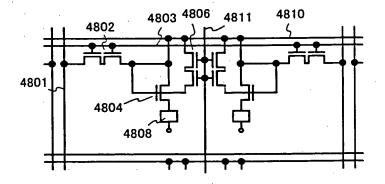
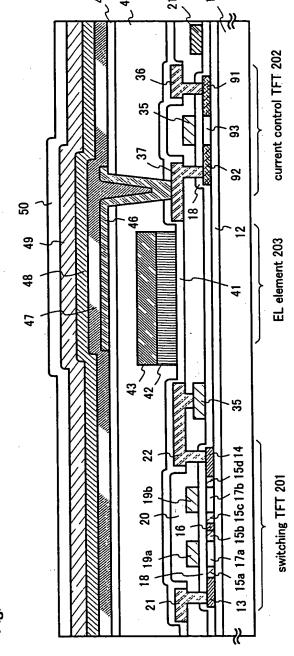


Fig. 22B





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